

# ROMiCE mini ROMiCEmini H8



ROMiCEmini is a real-time debugging tool incorporating ROM-in-circuit method. ROMiCEmini has been materialized to ultra compact body by the new technology incorporated while maintaining functionalities of existing ROMICE64. The use of USB2.0 (high-speed) as a host interface allows faster processing of various tasks such as high-speed downloading at 10MByte/S or higher rate. Furthermore, it is equipped with dedicated connector for connection to the target as standard, which was optional to existing ROMICE64. This allows instant connection. Yet, it supports ROM socket connection. "CSIDE", Computex-made debugger software provided as standard, surely provides a user-friendly debugging environment and supports not only high-level language debugging of a range of C languages but also fully prepared for the debugging of a range of RTOSs.

- Compact and instant connection to the target with dedicated connector<sup>\*1</sup>
- Takes less than 50ns to access emulation memory
- Equipped with hardware break and real-time tracing features as standard
- Automatically follows the target voltage 1.8V - 3.3V; 5V-tolerant
- Perfect for debugging of ASIC microcomputer without on-chip debugging feature
- Supports debugging of a range of RTOSs (Optional)
- Supports USB2.0 (High-speed), allowing high-speed processing
- Installed with emulation memory of 4MBytes
- Palm-sized ultra compact body
- Supports all of Renesas-made H8/300H and H8S series
- Supports MITSUMI-made MM8000 (Optional)

\*1: 32/40/42-pinned ROM socket type are also available

## Main Specifications

### Supported CPU

All of Renesas-made H8/300H series, all of H8S series  
(Does not support debugging of the target used in single-chip mode.)  
MITSUMI-made MM8000<sup>†</sup>

### Target Specifications

Target I/F	Dedicated connector	50-pinned dedicated connector
	ROM socket	32/40/42-pinned ROM socket (any 1 set of your choice)
Supported ROM	Bus size	8/16-bit
	Full capacity	4Mbytes
	Access speed	50ns
Supported voltage	NMIOU: 1.8V-5V (Automatic adaptive type); RSTOUT: Open-collector Other signals: 1.8V-3.3V (Automatic adaptive type, but 5V-tolerant for input)	

### Functional Specifications

Debugger provided as standard	CSIDE for ROMiCEmini H8-E	
Break features	Event break	2 points (Point conditions for Address, Data, and Status)
	Software break	256 points
	Force break	Utilizes NMI signal.
	External force break	Breaks at rising/trailing edge of external signal.
	Other breaks	Trace-end break/Interval timeout break/Write-protection break/Sequential break by 2 events
Tracing feature	Memory capacity	8K cycles x 50 bits
	Tracing mode	Free /Multiple/Normal/Sample
	Trace clock	20MHz(MAX)/50MHz asynchronous
Execution time measurement	Measures from execution to break by 1us unit up to 4294s for the maximum.	
Profiling feature	Measures frequency of function execution in real time and shows in graph.	
Interval execution time measurement feature	Measures time interval of execution between 2 points and shows the maximum/minimum value.	
Module execution time measurement	1 module 1Shot/Continue	

### Main Unit Specifications

Model	16M161
Outside dimensions	106mm(W) x 78mm(D) x 29.5mm(H)
Host I/F	USB mini-B (5-pinned) connector
Current consumption	DC5V Approx. 600mA MAX.
Weight	Approximately 160g
Operating environment	Operating temperature: 5 to 40 degrees Celsius Operating humidity: 35 to 85%RH No condensation
AC adapter (Accessory)	5V/2A

\*†: CPU library is required.

<b>Product composition contents</b>	<ul style="list-style-type: none"> <li>• ROMiCEmini main unit</li> <li>• Dedicated debugger (CD-ROM)</li> <li>• ROMiCEmini H8 User's Manual</li> <li>• License Tool Manual</li> <li>• Status 1-8CP probe (200mm)</li> <li>• ROM socket (Any 1 set: 32/40/42-pinned type)</li> <li>• USB2.0 cable (2m)</li> <li>• Stand</li> <li>• AC adapter(5V/2A)</li> </ul>
<b>Support System</b>	Yes

Operating environment	
	Recommended operating environment
<b>CPU</b>	Core2Duo equivalent or higher
<b>Installed memory</b>	2048MBytes or larger <sup>*1</sup>
<b>Hard disk</b>	Free space of 150MBytes or more is required.
<b>Supported USB</b>	USB2.0

\*1: Preferable to allow a triple size of a file for debugging.

Supported OSs (English)	
Supported OS	Remarks
Windows 2000 Professional	64-bit version is not supported.
Windows XP	64-bit version is not supported.
Windows Vista	64-bit version is not supported.

Supported languages, supported RTOSs	
<b>Supported compiler (H8 series)<sup>*1</sup></b>	Renesas C, XCC-V/H8, EW H8
<b>Supported RTOS (H8 series)<sup>*2</sup></b>	HI2000/3, NORTi Version4

\*1: For supported versions and other details, please contact us.

\*2: Optional dedicated debug libraries for respective RTOSs are required.

Optional CSIDE	
Product name	Description
<b>CSIDE for ROMiCEmini H8</b>	Optional CSIDE for debugging of H8 series CPUs(Japanese Edition).
<b>CSIDE for ROMiCEmini 80</b>	Optional CSIDE for debugging of 80 series CPUs(Japanese Edition).
<b>CSIDE for ROMiCEmini 80-E</b>	Optional CSIDE for debugging of 80 series CPUs(English Edition).
<b>CSIDE for ROMiCEmini TX19A</b>	Optional CSIDE for debugging of TX19 series CPUs(Japanese Edition).
<b>CSIDE for ROMiCEmini TLCS900H1</b>	Optional CSIDE for debugging of TLCS900H1 series CPUs(Japanese Edition).

Optional products	
Product name	Description
<b>RIM-ROMS32P</b>	32-pinned ROM socket set (MAIN socket, SUB socket, MAIN-SUB connection cable)
<b>RIM-ROMS40P</b>	40-pinned ROM socket
<b>RIM-ROMS42P</b>	42-pinned ROM socket
<b>RIM-STPRB2-15CP</b>	Status 2-15CP probe (Probe for event/trace signal connection, 500mm, connector-clip design, number of clips: 15)
<b>RIM-STPRB2-15CN</b>	Status 2-15CN probe (Probe for event/trace signal connection, 500mm, connector-connector design)
<b>RIM-EXPRB-4CP</b>	External -4CP probe (Probe for trigger output/external break input connection, 500mm, connector-clip design, number of clips: 4)
<b>RIM-EXPRB-4CN</b>	External -4CN probe (Probe for trigger output/external break input connection, 500mm, connector-connector design)
<b>MIT8-CPULIB-H8/RIM<sup>*1</sup></b>	CPU library for MITSUMI-made MM8000
<b>NORTiV4-DBGLIB-H8/RIM<sup>*1</sup></b>	RTOS debug library NORTi Version4
<b>HI2000-DBGLIB-H8/RIM<sup>*1</sup></b>	RTOS debug library HI2000/3

\*1: To use CPU library/RTOS debug libraries, you need to acquire license keys. License key is required for each ROMiCEmini main unit.

Notes on the use	
1.	Basically requires the target system that can run on ROM/RAM basis at least.
2.	Must allow the monitor program of 2K bytes always resident in the user memory.
3.	Must allow allocation of working RAM for monitor program use (H8S: 60 bytes (MIN), H8/300H: 48 bytes (MIN)).
4.	Must allow NMI pin connection.(Essential for hardware break feature)
5.	Must have 1 exception vector free (for use in software break feature)
6.	Advisable to connect reset output signal from ROMiCEmini to the target

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